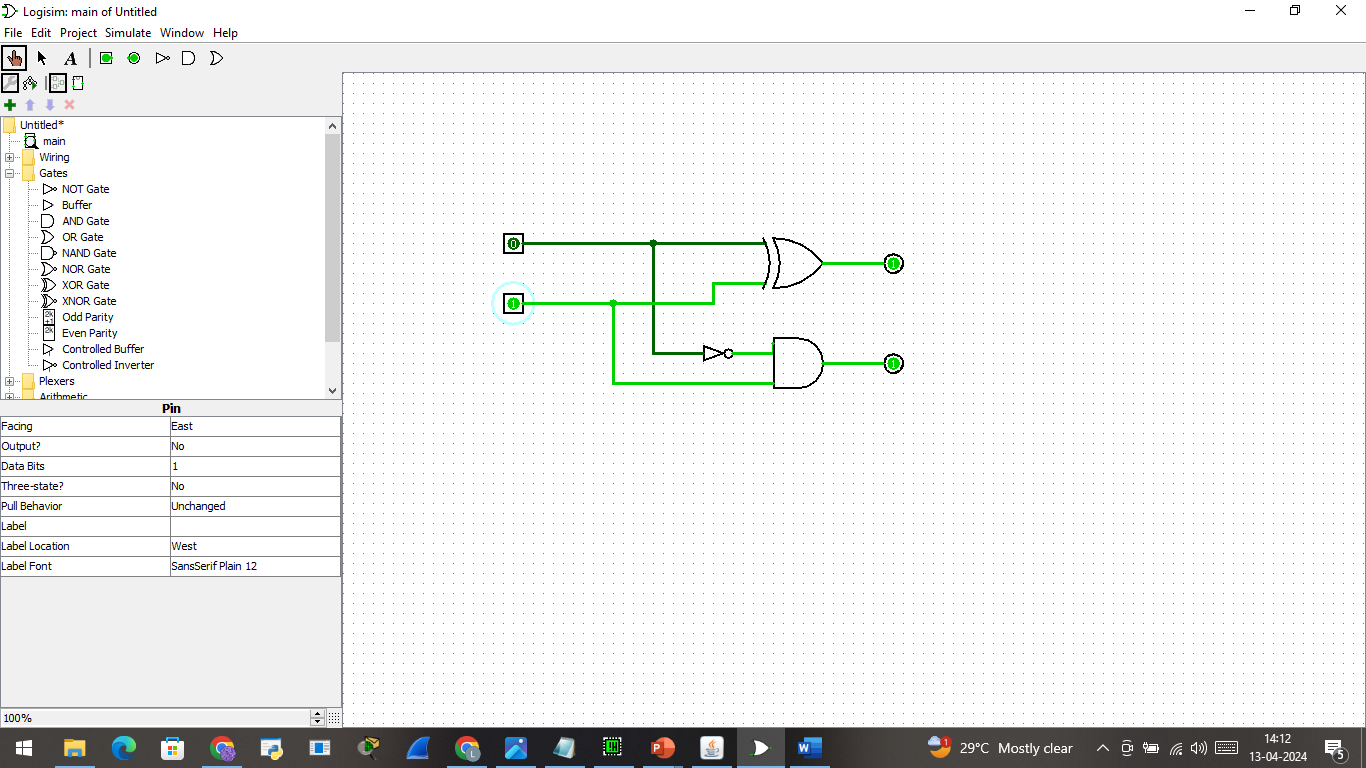
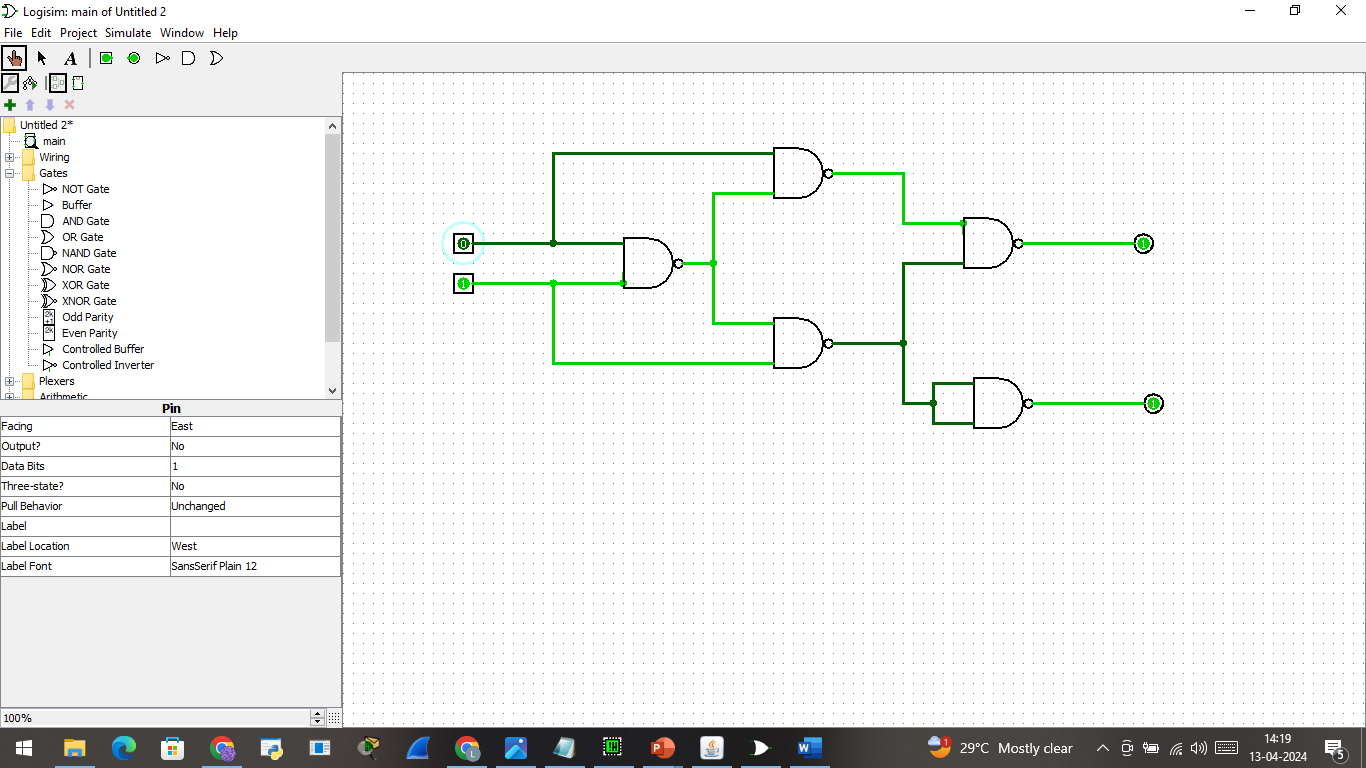
**Half subtractor**

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**Half subtractor- using normal gates**



**Half subtractor- using NAND gates**



**Half Subtractor- using NOR gates**

